

FIG.1

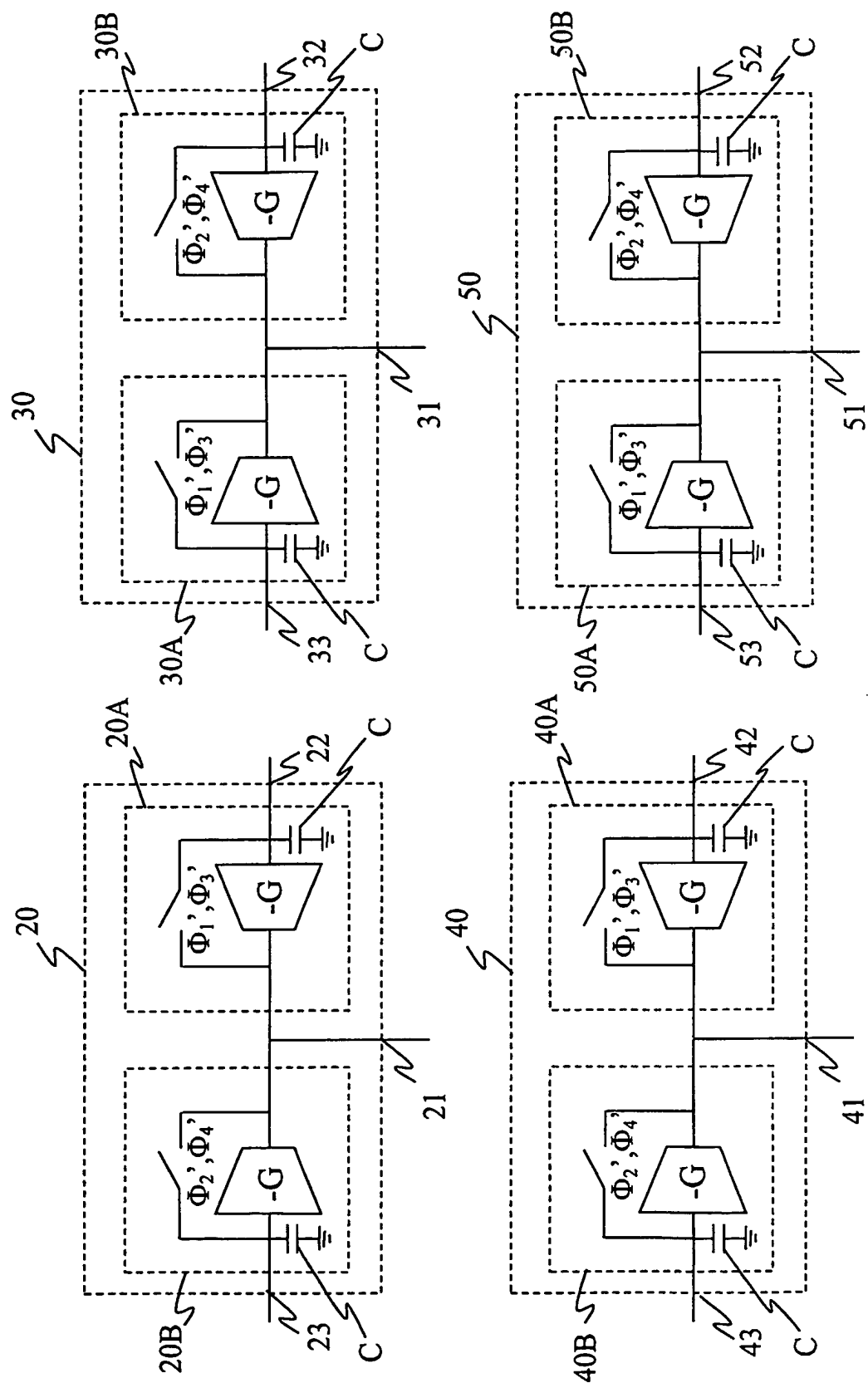


FIG. 2

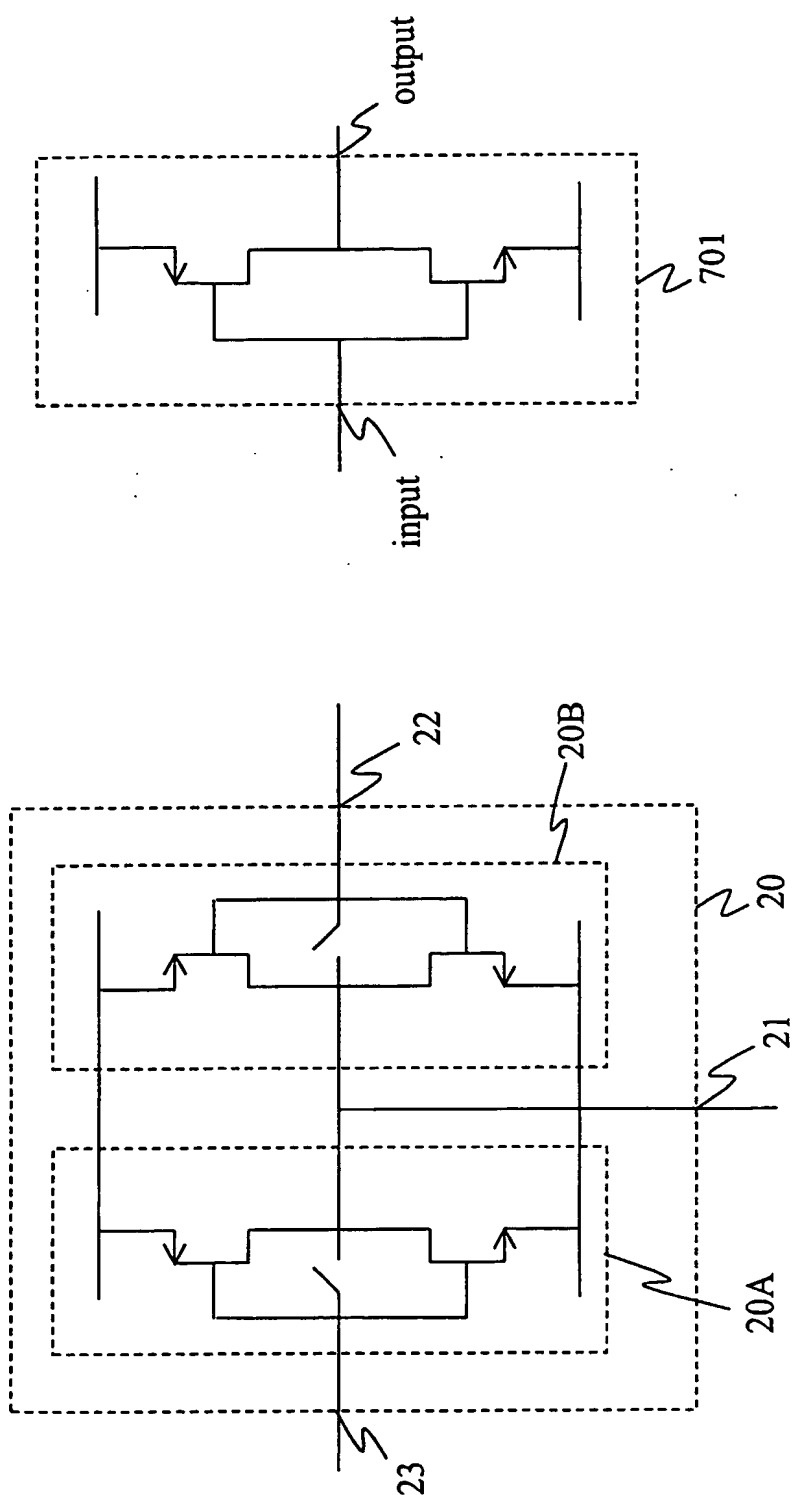


FIG. 6

FIG. 3

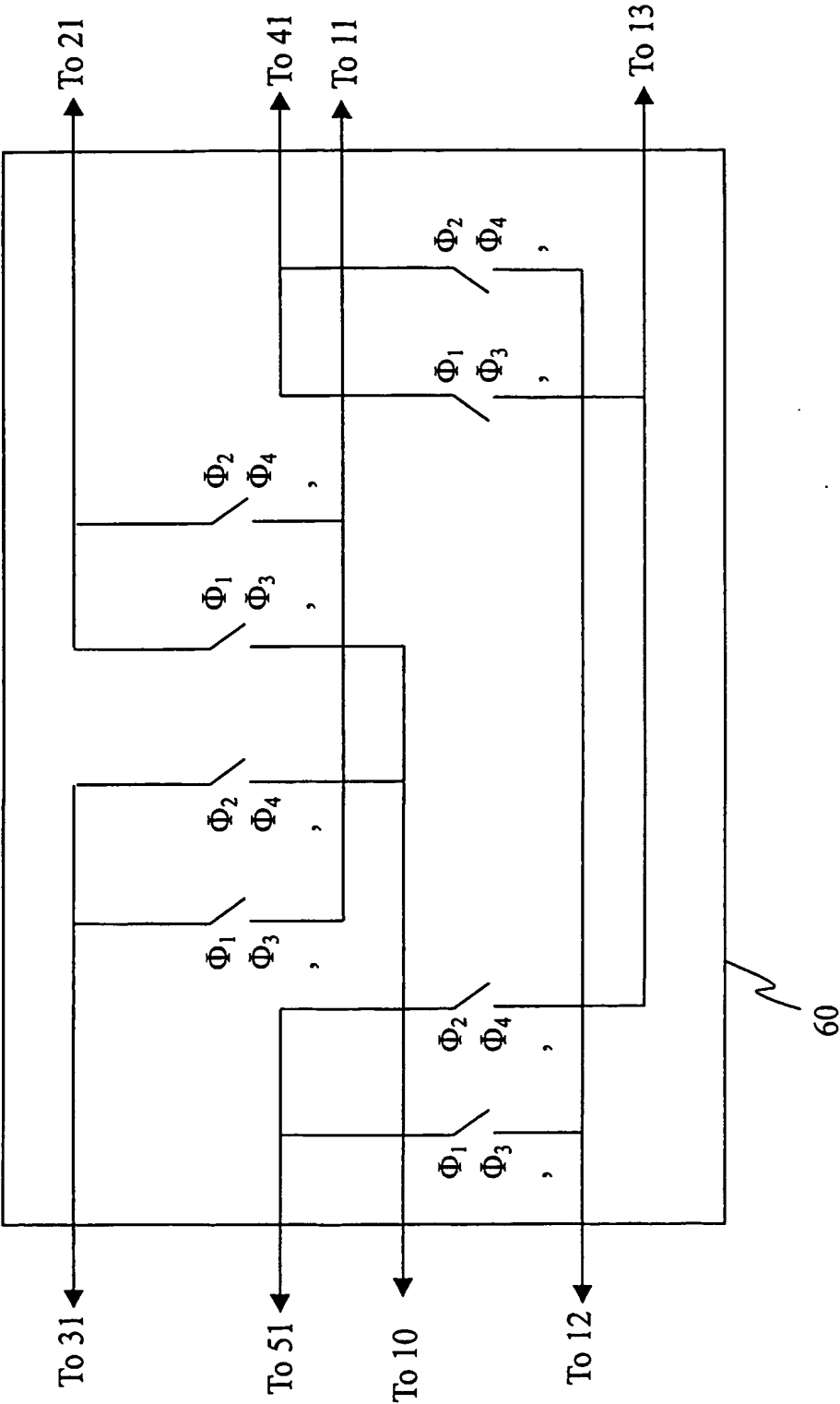
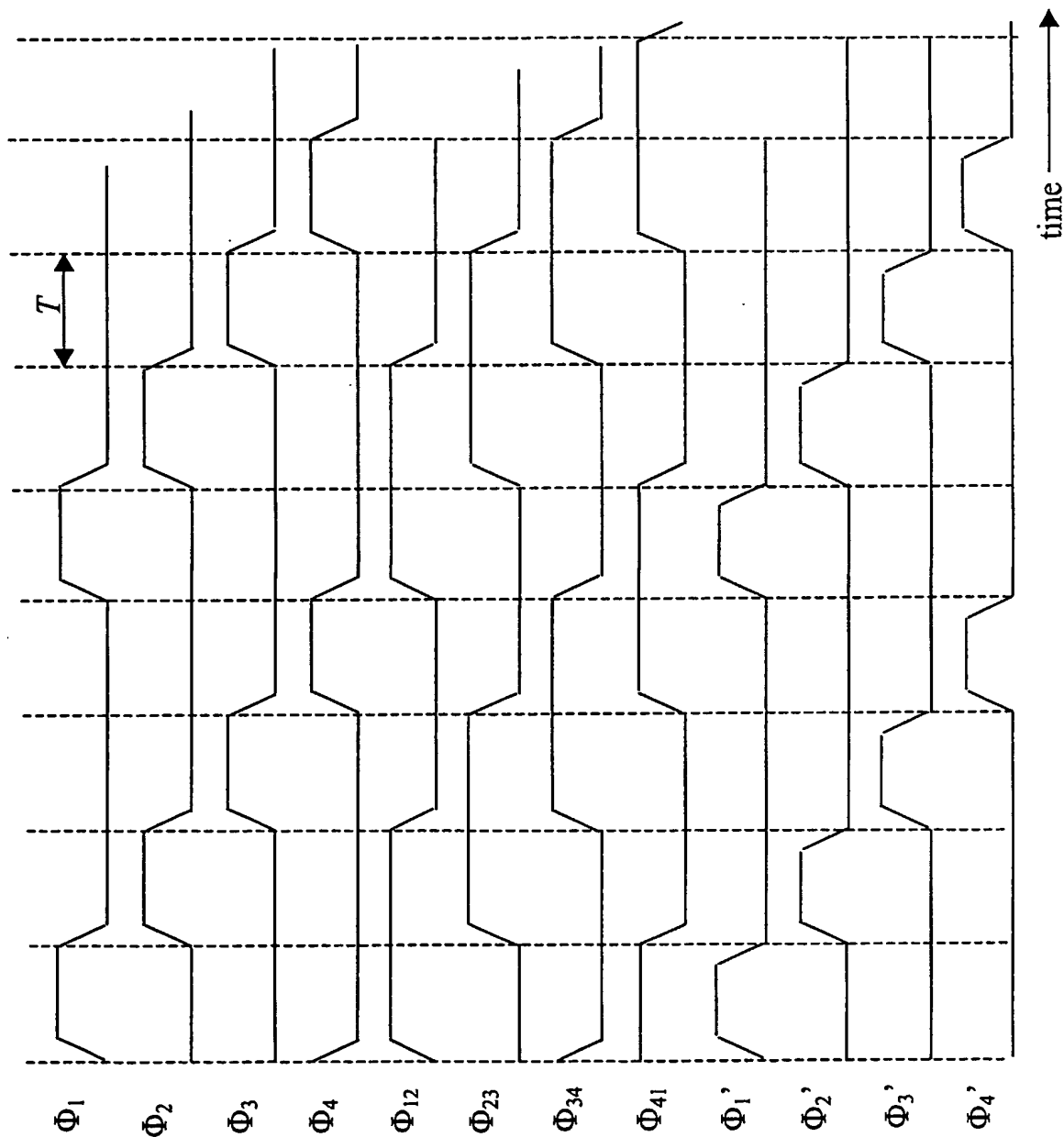


FIG.4

FIG.5



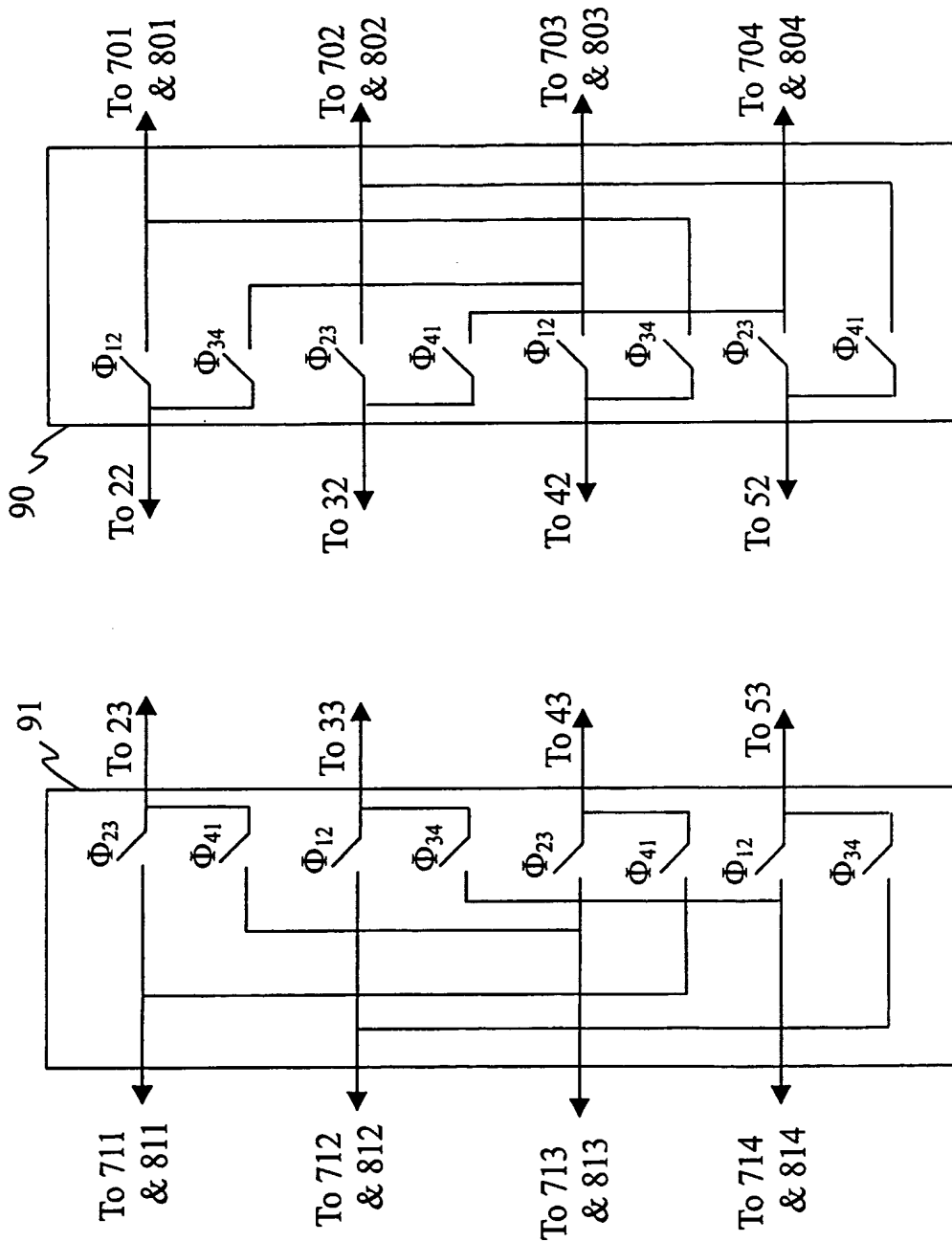


FIG. 7

Second output of integrator core-circuit	Scaling circuits coupled to specified second output of integrator core-circuit in specified period							
	First scale factor scaling circuit				Second scale factor scaling circuit			
	Φ_{12}	Φ_{23}	Φ_{34}	Φ_{41}	Φ_{12}	Φ_{23}	Φ_{34}	Φ_{41}
23		711		713		811		813
33	712		714		812		814	
43		713		711		813		811
53	714		712		814		812	

First output of integrator core-circuit	Scaling circuits coupled to specified first output of integrator core-circuit in specified period							
	First scale factor scaling circuit				Second scale factor scaling circuit			
	Φ_{12}	Φ_{23}	Φ_{34}	Φ_{41}	Φ_{12}	Φ_{23}	Φ_{34}	Φ_{41}
22	701		703		801		803	
32		702		704		802		804
42	703		701		803		801	
52		704		702		804		802

FIG.8

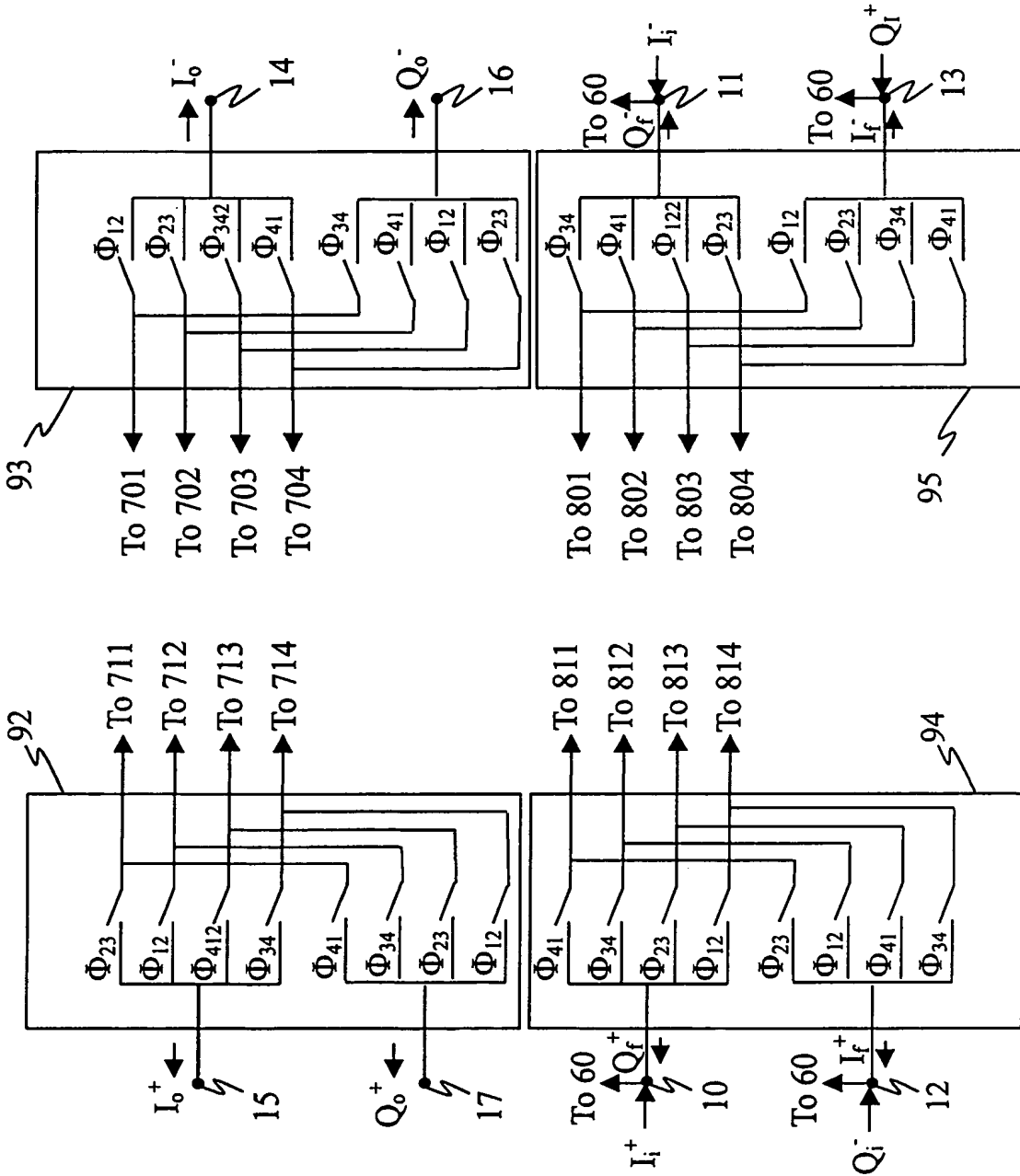


FIG. 9

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	Scaling circuits coupled to specified signal output in specified period			
	Φ_{12}	Φ_{23}	Φ_{34}	Φ_{41}
First signal output 14 (I_o^-)	701	702	703	704
Second signal output 15 (I_o^+)	712	711	714	713
Third signal output 16 (Q_o^-)	703	704	701	702
Fourth signal output 17 (Q_o^+)	714	713	712	711

	Scaling circuits coupled to specified signal input in specified period			
	Φ_{12}	Φ_{23}	Φ_{34}	Φ_{41}
First signal input 10	814	813	812	811
Second signal input 11	803	804	801	802
Third signal input 12	812	811	814	813
Fourth signal input 13	801	802	803	804

FIG.10

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FIG.11

Period	I_o^-		I_o^+		Q_o^+		Q_o^-	
	Sample-and-hold circuit	Scaling circuit	Sample-and-hold circuit	Scaling circuit	Sample-and-hold circuit	Scaling circuit	Sample-and-hold circuit	Scaling circuit
Φ_1	20A-S	701	30A-S	712	50A-S	714	40A-S	703
	30B-H	704	20B-H	713	40B-H	711	50B-H	702
Φ_2	30B-S	702	20B-S	711	40B-S	713	50B-S	704
	20A-H	701	30A-H	712	50A-H	714	40A-H	703
Φ_3	20A-S	703	30A-S	714	50A-S	712	40A-S	701
	30B-H	702	20B-H	711	40B-H	713	50B-H	704
Φ_4	30B-S	704	20B-S	713	40B-S	711	50B-S	702
	20A-H	703	30A-H	714	50A-H	712	40A-H	701

FIG.12

Period	Q_f^+		Q_f^-		I_f^+		I_f^-	
	Sample-and-hold circuit	Scaling circuit	Sample-and-hold circuit	Scaling circuit	Sample-and-hold circuit	Scaling circuit	Sample-and-hold circuit	Scaling circuit
Φ_1	50A-S	814	40A-S	803	30A-S	812	20A-S	801
	40B-H	811	50B-H	802	20B-H	813	30B-H	804
Φ_2	40B-S	813	50B-S	804	20B-S	811	30B-S	802
	50A-H	814	40A-H	803	30A-H	812	20A-H	801
Φ_3	50A-S	812	40A-S	801	30A-S	814	20A-S	803
	40B-H	813	50B-H	804	20B-H	811	30B-H	802
Φ_4	40B-S	811	50B-S	802	20B-S	813	30B-S	804
	50A-H	812	40A-H	801	30A-H	814	20A-H	803

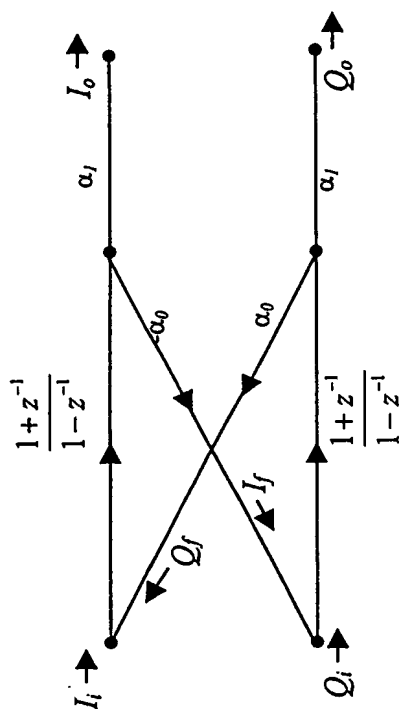


FIG.14

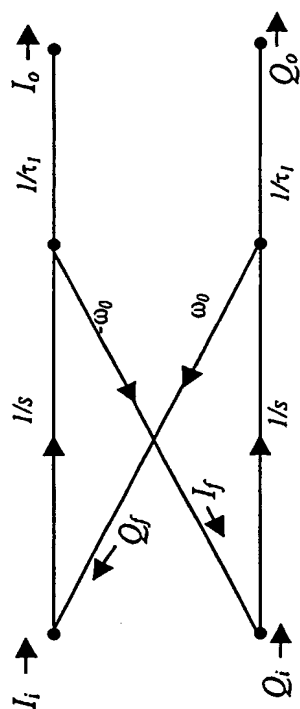


FIG.13

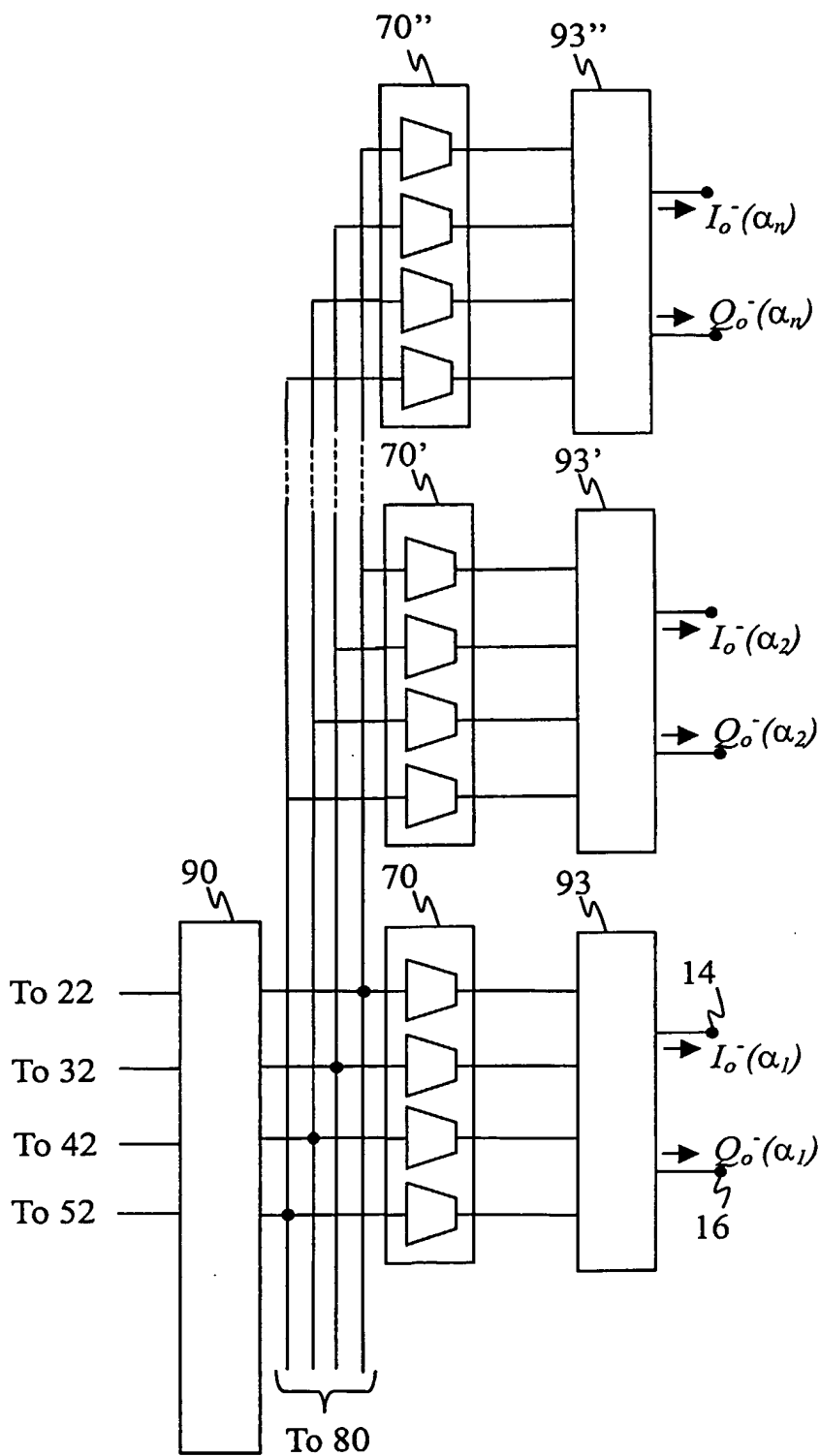


FIG.15

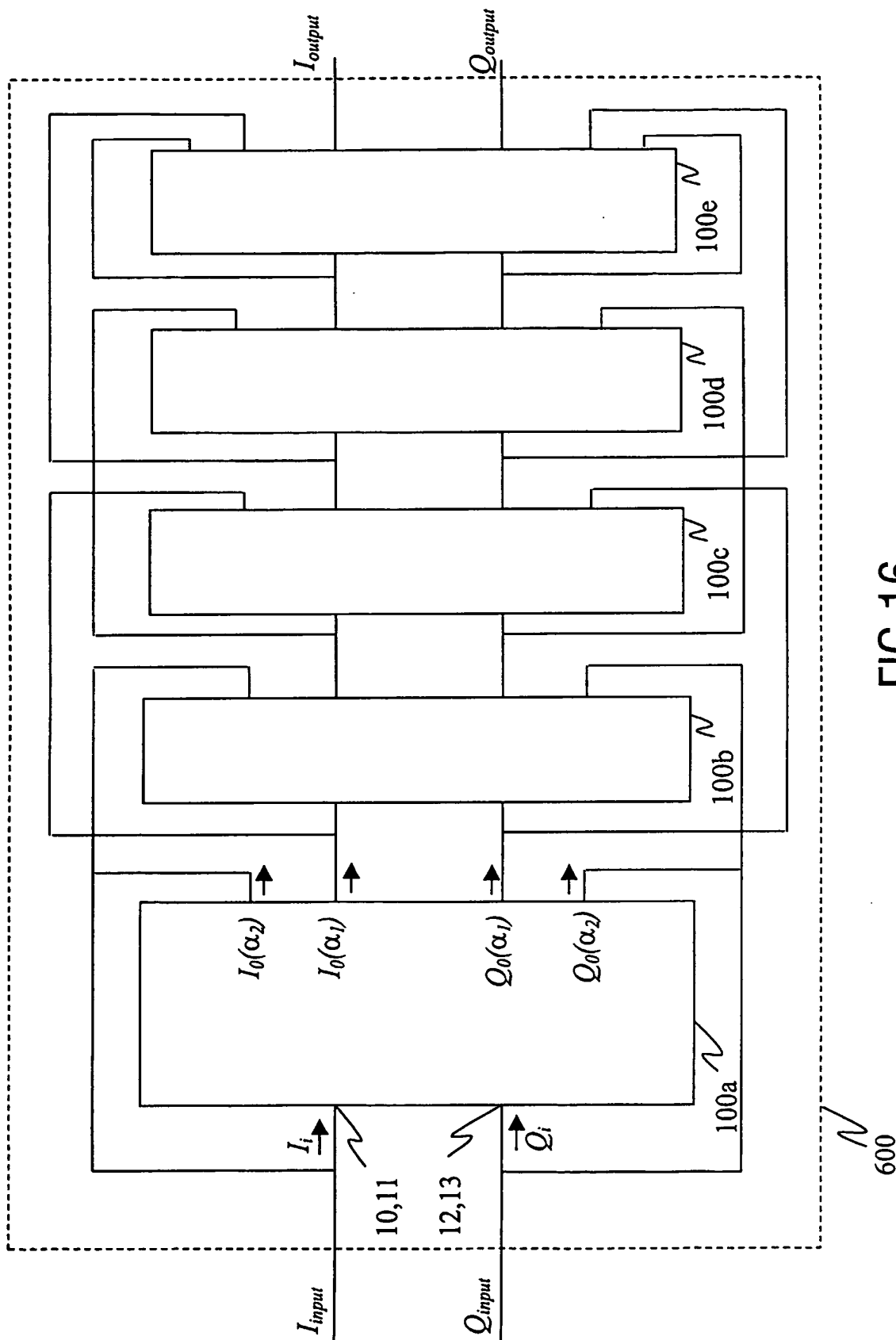


FIG.16

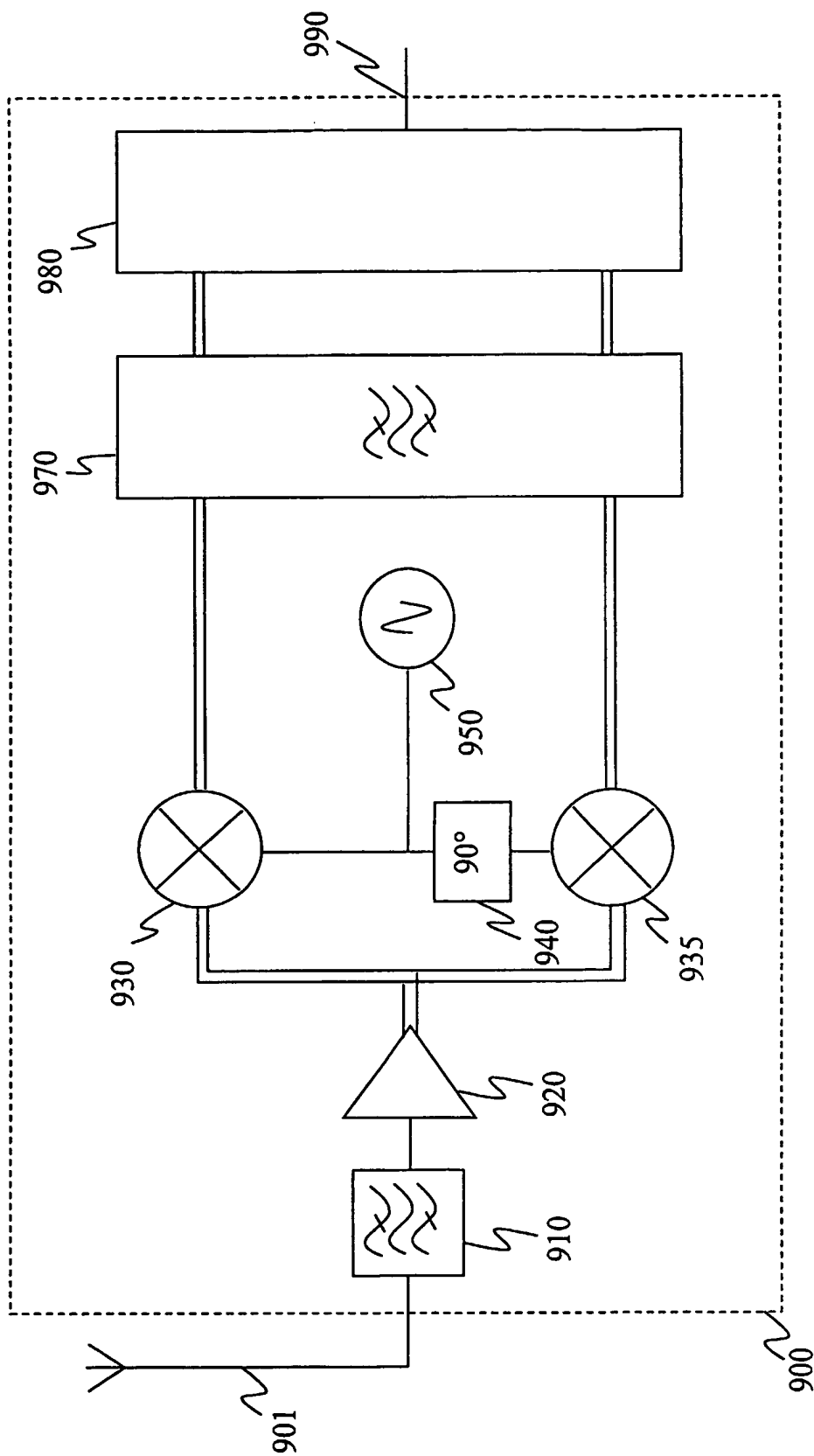


FIG.17